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PATENTS

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of

Romain DESPLATS et al.

Serial No. (unknown)

Filed herewith

METHOD AND INSTALLATION FOR  
FAST LOCATION OF A FAULT IN  
AN INTEGRATED CIRCUIT

**INFORMATION DISCLOSURE STATEMENT**

Assistant Commissioner for Patents

Washington, D.C. 20231

Sir:

In compliance with Rules 1.97 and 1.98, and in fulfillment of the duty of disclosure under Rule 1.56, the accompanying documents, copies of which are attached to this statement, are made of record on the enclosed sheet.

A concise explanation of the relevance of these items is that these references were cited by the European Patent Office in the corresponding International application Serial No. PCT/FR99/01212, filed May 21, 1999.

Respectfully submitted,

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